

<b>Information Disclosure Statement (IDS)</b>	Application No.:	
	Appl'n Filing Date:	March 29, 2004 (herewith)
	Examiner:	
	Art Unit:	
	Attorney Docket:	TI-36611
	First Inventor:	Heng-Chih LIN <i>et al.</i>
	Title of Invention:	CURRENT INTERPOLATION IN MULTI-PHASE LOCAL OSCILLATOR FOR USE WITH HARMONIC REJECTION MIXER

Mail Stop Patent Application  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Applicants draw the examiner's attention to the documents listed on the accompanying Form PTO-1449:

- The **Weldon *et al.*** reference is discussed in the *Background* section of the captioned patent application; part of the reference is illustrated in Applicants' "Prior Art" FIG. 1.
- The **Magoon *et al.*** reference uses a higher VCO frequency with a frequency divider and signal summation, to construct a pseudo-sinusoidal LO wave (Fig. 8) having reduced harmonic content; similar to the Weldon *et al.* reference, Magoon's approach has a high VCO frequency and a limited number of LO phases.

Applicants request that the examiner initial the space adjacent the documents on the accompanying Form PTO-1449 to indicate official consideration of the documents, and mail the initialed Form PTO-1449 to the correspondence address of record.

Respectfully submitted,

Date: March 29, 2004

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By: 

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<b>Form PTO-1449</b>  <b>Information Disclosure Citation</b>	Attorney Docket <b>TI-36611</b>	Application No.
	Applicant <b>Heng-Chih LIN <i>et al.</i></b>	Examiner
	Application Filing Date <b>March 29, 2004</b>	Group Art Unit

## U.S. Patent Documents

Examiner Initials	Cite	Patent or Application Number	Date	Name	Class	Sub-Class	Filing Date
	A						
	B						
	C						
	D						
	E						
	F						
	G						

## Foreign Patent Documents

Examiner Initials	Cite	Document Number	Date	Country	Name	Class	Sub-Class	Translation
	L							Yes No
	M							Yes No
	N							Yes No
	O							Yes No
	P							Yes No

## Other Documents (Including Author, Title, Date, Pertinent Pages, etc.)

	Q	Weldon, J. A.; Rudell, J.C.; Lin, L; Narayanaswami, R. S.; Otsuka, M.; Dedieu, S; Tee, L.; Tsai, K-C; Lee, C-W; and Gray, P. R.; "A 1.75 GHz Highly-Integrated Narrow-Band CMOS Transmitter with Harmonic-Rejection Mixers," Section 10.4 of <i>Digest of Technical Papers of the 2001 IEEE International Solid-State Circuits (ISSC) Conference</i> , 5-7 Feb. 2001, pages 160-162.
	R	Magoon, R., Molnar, A., Zachan, J., Hatcher, G., and Rhee, W., "A single-chip quad-band (850/900/1800/1900MHz) Direct Conversion GSM/GPRS RF Transceiver with Integrated VCOs and Fractional-N Synthesizer," <i>IEEE Journal of Solid-State Circuits</i> , vol. 37, No. 12, December 2002, pp. 1710-1720.
	S	

Examiner Signature	Date Considered
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.